



UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ramchandran
Serial No. : 10/628,083
Filed : 07/24/2003
Title : CACHE FOR INSTRUCTION SET ARCHITECTURE USING INDEXES TO
ACHIEVE COMPRESSION

Art Unit : 2183
Examiner : Coleman, Eric

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

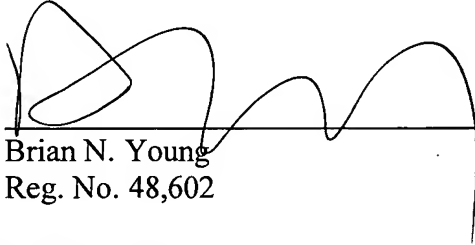
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Respectfully submitted,

Date: April 7, 2006


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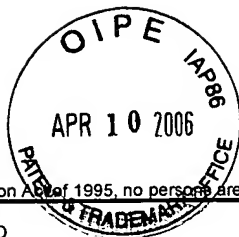
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/628,083
Filing Date	7/24/2003
First Named Inventor	RAMCHANDRAN
Art Unit	2183
Examiner Name	Coleman, Eric
Attorney Docket Number	021202-003730US

Sheet 1 of 1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	AA	US- 5734808	03-1998	Takeda, Masaki	
	AB	US- 6615333	09-2003	Hoogerbrugge et al.	
	AC	US- 6154494	11-2000	Araki et al.	
	AD	US- 6819140	11-2004	Yamanaka et al.	
	AE	US- 5835753	11-1998	Witt, David B.	
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	AG	US- 5534796	07-1996	Edwards, Stephen W.	
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	AJ	US- 2001-0029515	10-2001	Mirsky, Ethan A.	
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	AL	US- 6883074	04-2005	Donohoe, Gregory	
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		US-			
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FOREIGN PATENT DOCUMENTS

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